HA17458 Series

Dual Operational Amplifier

HITACHI

ADE-204-040 (Z)
Rev. 0
Dec. 2000

Description

HA17458 is dual operational amplifiers which provides internal phase compensation and high performance. It can be applied widely to measuring control equipment and to general use.

Features

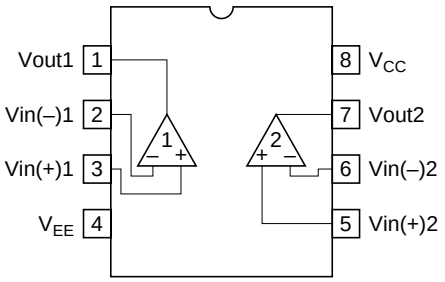
- High voltage gain: 100dB (Typ)
- Wide output amplitude: ±13V (Typ) [at $R_L \geq 2k\Omega$]
- Protected from output shortcircuit
- Internal phase compensation

Ordering Information

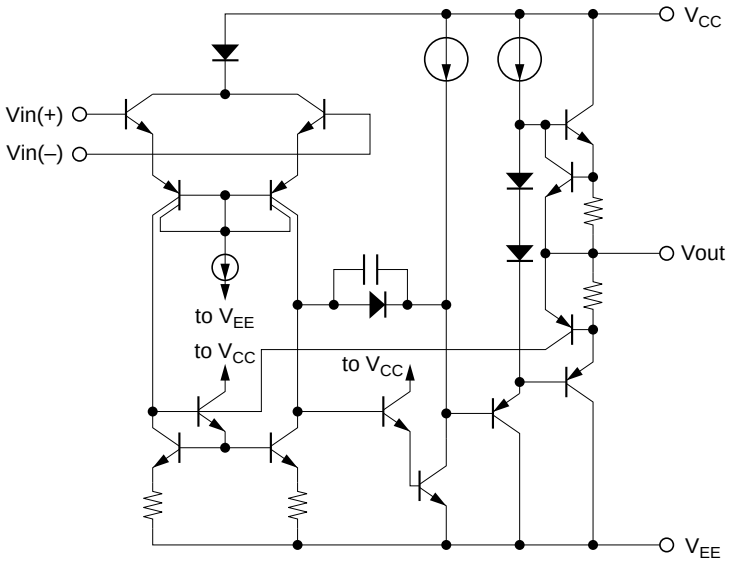
Type No.	Application	Package
HA17485FP	Industrial use	FP-8D
HA17458F	Commercial use	FP-8D
HA17458	Commercial use	DP-8
HA17458PS	Industrial use	DP-8

Hitachi semiconductor

Pin Arrangement



Circuit Schematic (1/2)



Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Ratings				Unit
		HA17458	HA17458PS	HA17458F	HA17458FP	
Supply voltage	V_{CC}	+18	+18	+18	+18	V
	V_{EE}	–18	–18	–18	–18	V
Intput voltage	V_{IN}*[3]	±15	±15	±15	±15	V
Differential input voltage	$V_{IN(diff)}$	±30	±30	±30	±30	V
Power dissipation	P_T	670*[1]	670*[1]	385*[2]	385*[2]	mW
Operating temperature	Topr	–20 to +75	–20 to +75	–20 to +75	–20 to +75	°C
Storage temperature	Tstg	–55 to +125	–55 to +125	–55 to +125	–55 to +125	°C

Notes: 1. These are the allowable values up to Ta = 45 °C. Derate by 8.3mW/°C above that temperature.
2. These are the allowable values up to Ta = 31 °C mounting on 30% wiring density glass epoxy board. Derate by 7.14mW/°C above that temperature.
3. If the supply voltage is less than ±15V, input voltage should be less than supply voltage.

Electrical Characteristics 1 (V_{CC} = –V_{EE} = 15V, Ta = 25°C)

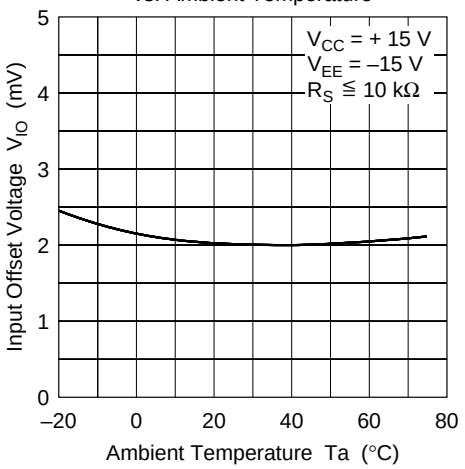
Item	Symbol	Min	Typ	Max	Unit	Test conditions
Input offset voltage	V_{IO}	—	2.0	6.0	mV	R_S ≤ 10kΩ
Input offset current	I_{IO}	—	6	200	nA	
Input bias current	I_{IB}	—	30	500	nA	
Line regulation	$\Delta V_{IO}/\Delta V_{CC}$	—	30	150	µV/V	R_S ≤ 10kΩ
	$\Delta V_{IO}/\Delta V_{EE}$	—	30	150	µV/V	R_S ≤ 10kΩ
Voltage gain	A_{VD}	86	100	—	dB	R_L ≥ 2kΩ, Vout = ±10V
Common mode rejection ratio	CMR	70	90	—	dB	R_S ≤ 10kΩ
Common mode input voltage range	V_{CM}	±12	±13	—	V	
Peak-to-peak output voltage	Vop-p	±12	±14	—	V	R_L = 10kΩ
Power dissipation	P_d	—	90	200	mW	No load, 2 channel
Slew rate	SR	—	0.6	—	V/µs	A_{VD} = 1
Input resistance	Rin	0.3	1.0	—	MΩ	
Input capacitance	Cin	—	6.0	—	pF	
Output resistance	Rout	—	75	—	Ω	

Electrical Characteristics 2 (V_{CC} = –V_{EE} = 15V, Ta = –20 to +75°C)

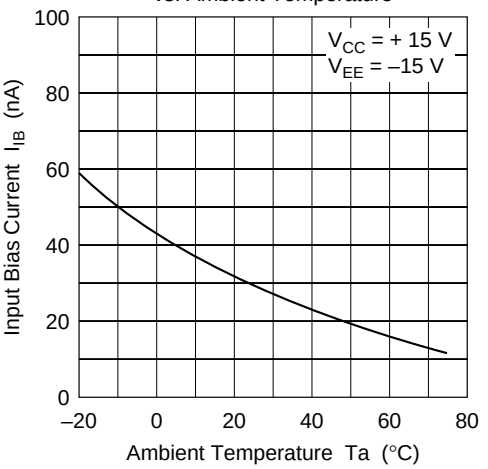
Item	Symbol	Min	Typ	Max	Unit	Test conditions
Input offset voltage	V_{IO}	—	—	9.0	mV	R_S ≤ 10kΩ
Input offset current	I_{IO}	—	—	400	nA	
Input bias current	I_{IB}	—	—	1100	nA	
Voltage gain	A_{VD}	80	—	—	dB	R_L ≥ 2kΩ, Vout = ±10V
Peak-to-peak output voltage	Vop-p	±10	±13	—	V	R_L = 2kΩ

Characteristic Curves

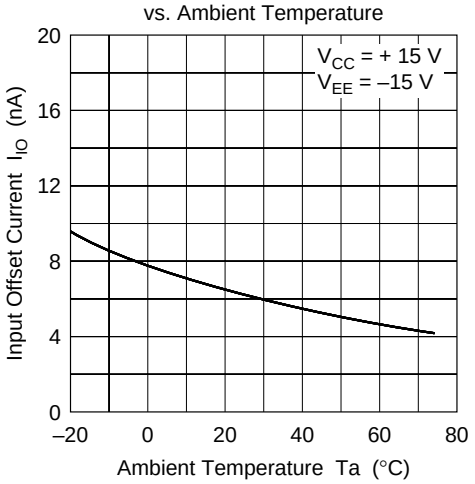
Input Offset Voltage vs. Ambient Temperature



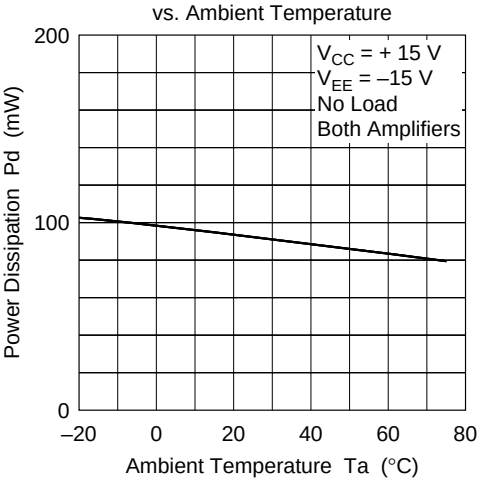
Input Bias Current vs. Ambient Temperature



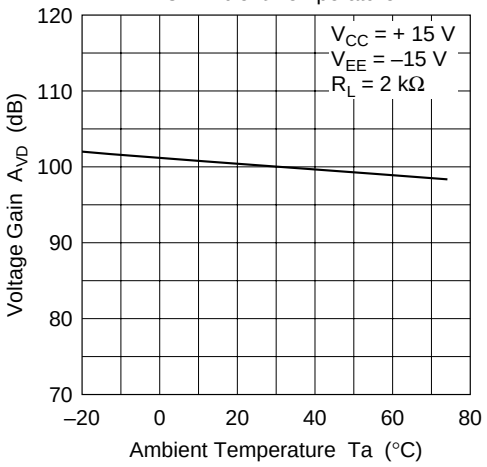
Input Offset Current vs. Ambient Temperature



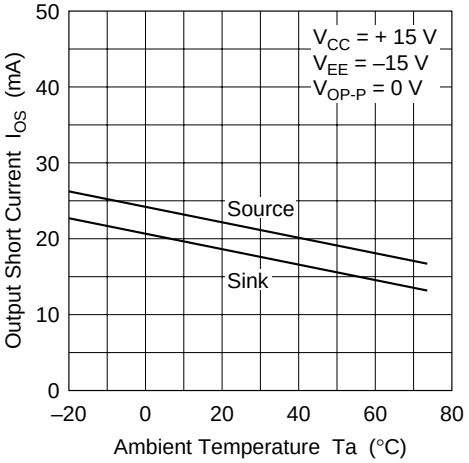
Power Dissipation vs. Ambient Temperature



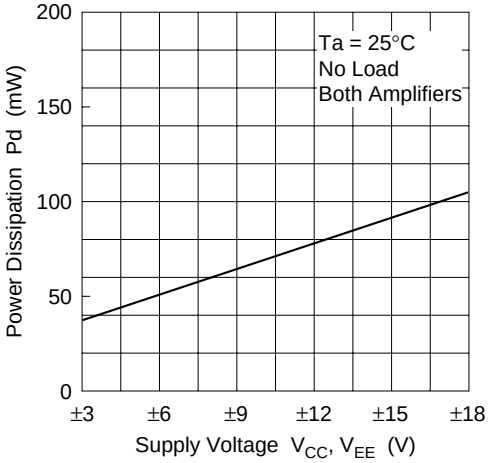
Voltage Gain
vs. Ambient Temperature



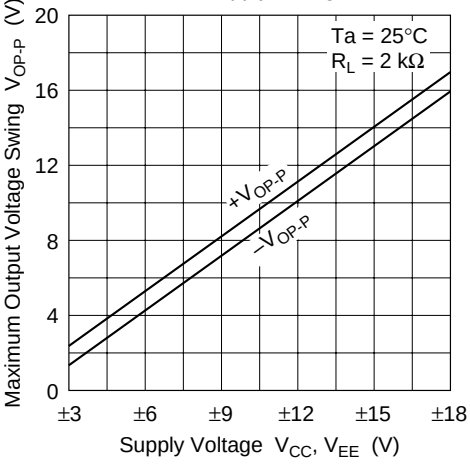
Output Short Current
vs. Ambient Temperature



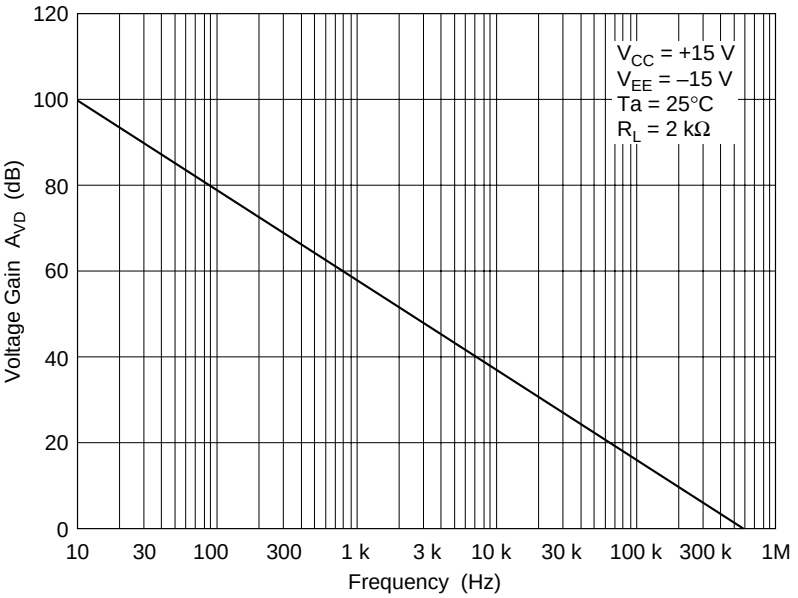
Power Dissipation
vs. Supply Voltage



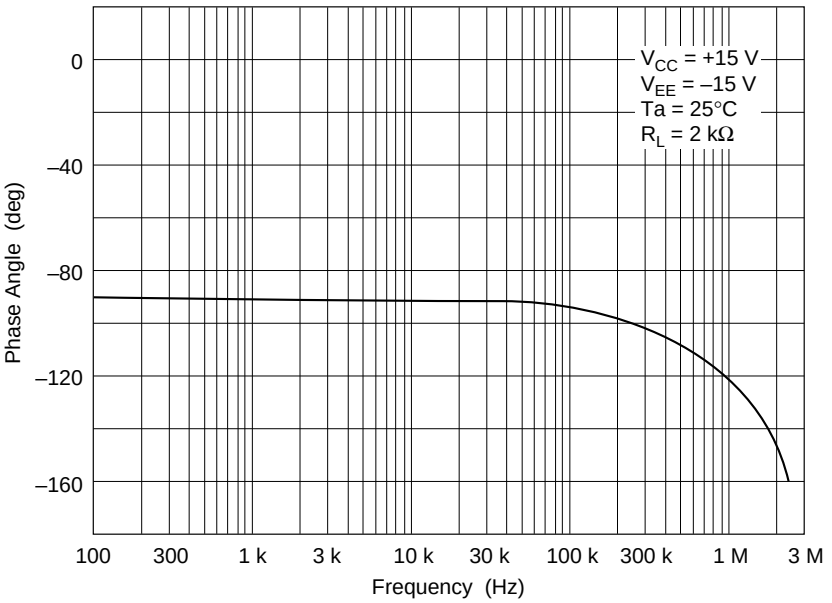
Maximum Output Voltage Swing
vs. Supply Voltage



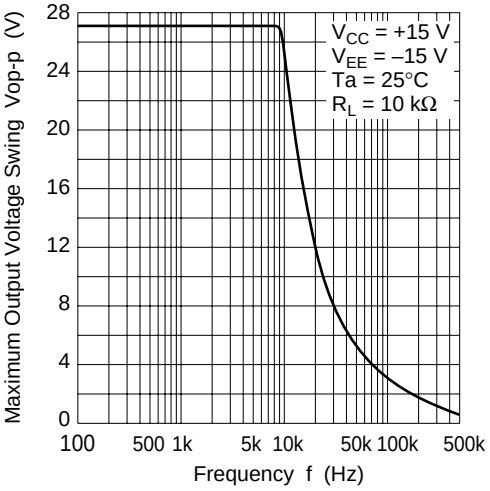
Voltage Gain vs. Frequency



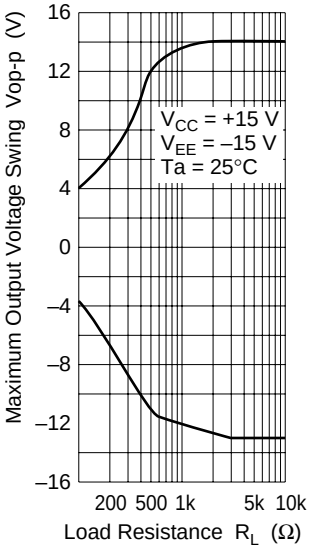
Phase Angle vs. Frequency



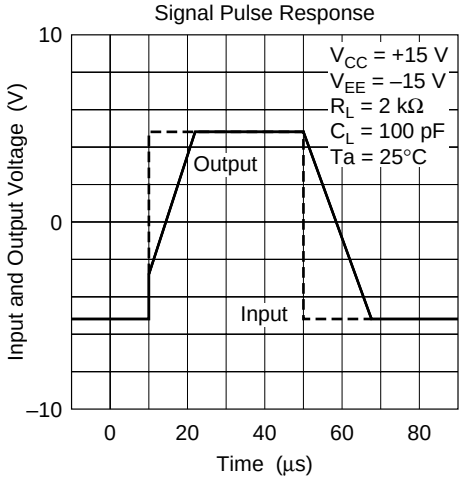
Maximum Output Voltage Swing vs. Frequency



Maximum Output Voltage Swing vs. Load Resistance

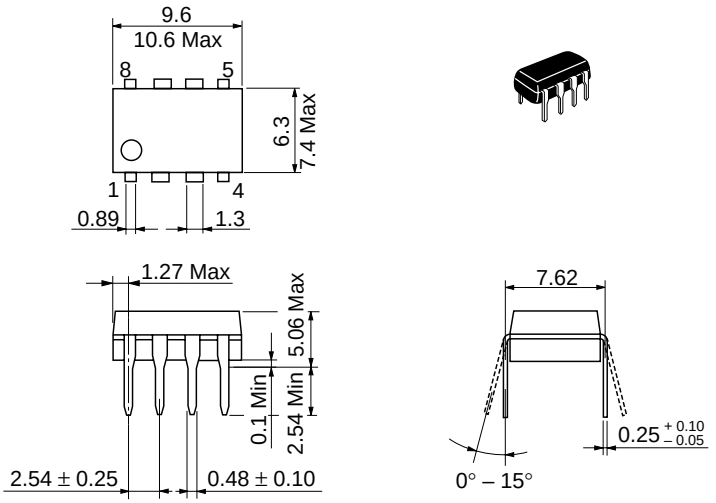


Voltage Follower Large Signal Pulse Response



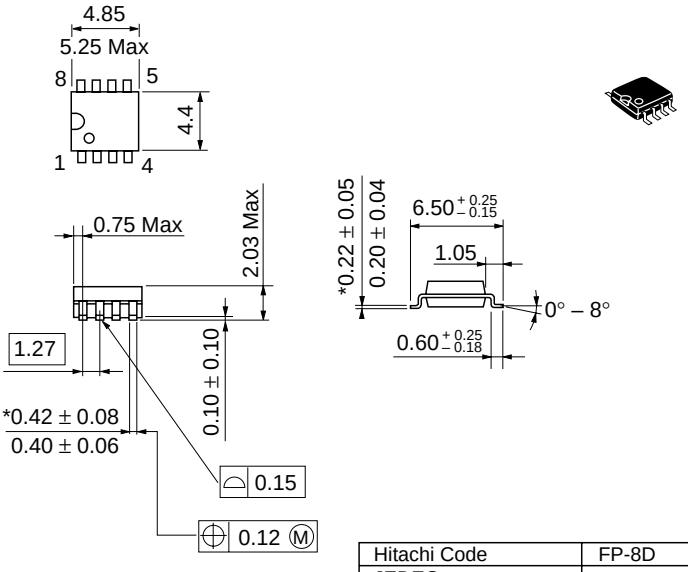
Package Dimensions

Unit: mm



Hitachi Code	DP-8
JEDEC	Conforms
EIAJ	Conforms
Mass (reference value)	0.54 g

Unit: mm



Hitachi Code	FP-8D
JEDEC	—
EIAJ	Conforms
Mass (reference value)	0.10 g

*Dimension including the plating thickness
Base material dimension

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HITACHI

Hitachi, Ltd.
Semiconductor & Integrated Circuits.
Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan
Tel: Tokyo (03) 3270-2111 Fax: (03) 3270-5109

URL
NorthAmerica : http://semiconductor.hitachi.com/
Europe : http://www.hitachi-eu.com/hel/ecg
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For further information write to:

Hitachi Semiconductor (America) Inc.
179 East Tasman Drive,
San Jose,CA 95134
Tel: <1> (408) 433-1990
Fax: <1>(408) 433-0223

Hitachi Europe GmbH
Electronic Components Group
Dornacher Straβe 3
D-85622 Feldkirchen, Munich
Germany
Tel: <49> (89) 9 9180-0
Fax: <49> (89) 9 29 30 00

Hitachi Europe Ltd.
Electronic Components Group.
Whitebrook Park
Lower Cookham Road
Maidenhead
Berkshire SL6 8YA, United Kingdom
Tel: <44> (1628) 585000
Fax: <44> (1628) 585160

Hitachi Asia Ltd.
Hitachi Tower
16 Collyer Quay #20-00,
Singapore 049318
Tel : <65>-538-6533/538-8577
Fax : <65>-538-6933/538-3877
URL : http://www hitachi.com.sg

Hitachi Asia Ltd.
(Taipei Branch Office)
4/F, No. 167, Tun Hwa North Road,
Hung-Kuo Building,
Taipei (105), Taiwan
Tel : <886>-(2)-2718-3666
Fax : <886>-(2)-2718-8180
Telex : 23222 HAS-TP
URL : http://www hitachi.com.tw

Hitachi Asia (Hong Kong) Ltd.
Group III (Electronic Components)
7/F., North Tower,
World Finance Centre,
Harbour City, Canton Road
Tsim Sha Tsui, Kowloon,
Hong Kong
Tel : <852>-(2)-735-9218
Fax : <852>-(2)-730-0281
URL : http://www.hitachi.com.hk

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